Abstract—Out-of-plane displacement (warpage) has been a major thermomechanical reliability concern for board-level electronic packages. Printed wiring board (PWB) and component warpage results principally from coefficient of thermal expansion mismatch among the materials that make up the PWB assembly (PWBA). Warpage occurring during surface-mount assembly reflow processes and normal operations may lead to severe solder bump reliability problems. In this research, the effect of initial PWB warpage on the low cycle thermal fatigue reliability of the solder bumps in plastic ball grid array (PBGA) packages was studied using experimental and analytical methods. A real-time projection moiré warpage measurement system was used to measure the surface topology of PBWA samples at different temperatures. The thermal fatigue reliability of solder bumps was evaluated from experimental thermal cycling tests and finite element simulation results. Three-dimensional (3-D) models of PWBAs with varying board warpage were used to estimate the solder bump fatigue life for different types of PBGAs mounted on PWBs. In order to improve the accuracy of FE results, the projection moiré method was used to measure the initial warpage of PWBs, and this warpage was used as a geometric input to the finite element method. The simulation results were validated and correlated with the experimental results obtained using the projection moiré technique and accelerated thermal cycling tests. An advanced prediction model was generated to predict board level solder bump fatigue life based on the initial PWB warpage, package dimensions and locations, and solder bump materials.

Index Terms—Fatigue, prediction model, solder bumps, warpage.

I. INTRODUCTION

WARPAGE is one of the major thermomechanical reliability concerns for board-level electronic packaging. Printed wiring board (PWB) and component warpage results from coefficient of thermal expansion (CTE) mismatch among the materials that make up PWB assemblies (PWBA) such as solder, copper, FR-4, encapsulation molding, and silicon. Warpage occurring during the surface-mount assembly reflow process and normal operations may cause serious reliability problems, such as severe solder bump fatigue failure, die cracking, and delamination of the solder bumps between electronic components and the PWB [1]. PWB/PWBA warpage can be measured by many different optical techniques, among which moiré methods have emerged as very accurate real-time, noncontact, full-field, superior techniques with high resolutions. In this paper, a warpage measurement system based on the projection moiré technique was used. The projection moiré method is complementary to the shadow moiré technique, and does not require a glass grating. Therefore, it is more suitable for measuring a PWB surface with assembled components, and it can easily be used to measure various board and package sizes by simply adjusting its grating sizes. The characteristics of the real-time projection moiré warpage measurement system are presented in this paper.

Fatigue is another common failure mechanism of electronic devices and is believed to be either fully or partially responsible for 90% of all structural and electrical failures. It is caused by cyclic thermomechanical stresses, and is severely affected by out-of-plane displacement of PWBs. This paper discusses the correlation between the initial PWB warpage and solder bump fatigue on board assemblies. The effect of the initial PWB warpage on the low cycle fatigue of the solder bumps on PBGA packages was studied in depth based on experimental study and finite element analysis. The projection moiré system and accelerated thermal cycling tests were used to evaluate the board level solder bump fatigue reliability affected by PWB warpage. Three-dimensional FE models of PWBAs with different initial board warpage were used to predict the solder bump fatigue life for different types of PBGAs on PWBs. Based on the experimental and simulation results, design of experiments and an advanced prediction model was developed to accurately investigate the correlation between the initial PWB warpage and solder bump fatigue life on board assemblies.

II. PROJECTION MOIRÉ MEASUREMENT SYSTEM

A. Projection Moiré System Optical Setup

The projection moiré optical setup is shown in Fig. 1. A diode-pumped laser with a wavelength of 532-nm is expanded and then enters a Michelson interferometer to create the grating pattern on the sample surface. The interferometer’s reference mirror is mounted on a piezoelectric transducer (PZT) for phase stepping to achieve high-resolution measurement. Two projected grating images captured by a charge-coupled device (CCD) camera at different time will be superposed to obtain moiré fringes. These fringes can then be used to calculate the surface topology of the PWB/PWBA. One image is the
reference grating pattern captured when the grating lines are projected onto a flat reference surface; the other image includes the deformed grating lines created by the PWB/PWBA surface being measured [2].

B. Out-of-Plane Displacement Calculation

The relationship between sample warpage and moiré fringes is shown below

\[ w = \frac{np}{\tan \alpha + \tan \beta} \]

where \( w \) is out-of-plane displacement at the \( n \)th fringe order; \( n \) is fringe order; \( p \) is grating pitch; \( \alpha \) observation angle; \( \beta \) is illumination angle.

For the projection moiré setup described above, the observation angle, \( \alpha \), is 0° and the illumination angle, \( \beta \), is 45°. The fringe order, \( n \), is not required to be an integer, and can be determined by \( n = \text{phase}/2\pi \). To obtain accurate warpage results, four-step phase stepping method is applied to achieve precise fringe order [3]. Then warpage of the sample surface can be calculated.

The resolution of the projection moiré system depends on the surface size being measured. Normally the CCD camera with 480 × 512 pixels can resolve 100 fringes in its full field. The four-step phase stepping technique can increase the measurement resolution by 100 times. Therefore, if the size of a sample surface is \( L \times L \), the resolution of the projection moiré measurement system is \( L/10,000 \).

III. PWBA TEST VEHICLE

The PWBA test vehicle used in this research is shown in Fig. 2. It is composed of a 203.2 mm × 139.7 mm × 0.631 mm PWB and up to two PBGA packages per board. Two types of PBGA packages were used in this study: one has 256 solder bumps, a bump pitch of 1.27 mm, and dimensions of 27 × 27 mm; the other has 352 solder bumps, a bump pitch of 1.27 mm, and dimensions of 35 × 35 mm. As shown in Fig. 2, the PBGA can be located at a horizontal distance of 60 mm and a vertical distance of 30 mm from the center of the PWB, or at a horizontal distance of 0 mm and a vertical distance of 30 mm from the center of the PWB. The PWB consists of four copper layers and three FR-4 layers. Fig. 3 shows a cross-section schematic of the layered PWB along with its thickness dimensions. Specifically, the top copper layer of the PWB contains traces, which are 127 μm thick and have a pitch of 508 μm.

IV. EXPERIMENTAL STUDY ON SOLDER BUMP FATIGUE RELIABILITY AFFECTED BY INITIAL PWB WARPAGE

In order to accurately set up a correlation between the initial PWB warpage and solder bump fatigue reliability on board assemblies, the accelerated thermal cycling tests were conducted using an ESPEC test chamber. The applied thermal cyclic condition was JESD22-A104-A with the temperature range between −40°C and 125°C every 20 min, keeping the dwell time as 10 min at the two temperature extremes.

There are a total of 20 PWBA packages with 27 × 27 mm or 35 × 35 mm PBGA packages used in this study. The projection moiré system as shown in Fig. 1 was used to measure the warpage of the 20 PWBA samples at room temperature before the thermal cycling tests. Each PWB has different initial board warpage as shown in Table I. Five boards among the test vehicles were assembled with 27 × 27 mm PBGA packages at location 1 respectively, which was at a horizontal distance of 60 mm and a vertical distance of 30 mm from the PWB center; five PWBA packages were assembled with 27 × 27 mm PBGA packages at location 2, respectively, which was at a horizontal distance of 0 mm and a vertical distance of 30 mm from the PWB center; five PWBA packages were assembled with 35 × 35 mm PBGA packages at location 1, respectively; five PWBA packages were assembled with 35 × 35 mm PBGA packages at location 2, respectively. Fig. 4 shows the warpage measurement results for the PWBA with one 35 × 35 mm PBGA at Location 1 by using the projection moiré system.
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TABLE I

<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
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<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Location 1</td>
<td>Location 2</td>
<td>Location 1</td>
<td>Location 2</td>
<td>Location 1</td>
<td>Location 2</td>
</tr>
<tr>
<td>27 × 27 mm PBGA, 63Sn-37Pb, 256 solder bumps</td>
<td>1706.4</td>
<td>1819.5</td>
<td>1754.2</td>
<td>1733.3</td>
<td>1997.0</td>
<td>2004.4</td>
</tr>
<tr>
<td>35 × 35 mm PBGA, 63Sn-37Pb, 352 solder bumps</td>
<td>2433.3</td>
<td>3076.6</td>
<td>2536.6</td>
<td>2741.8</td>
<td>3176.1</td>
<td>3824.0</td>
</tr>
</tbody>
</table>

Fig. 4. Warpage measurement plot of PWBA with one 35 × 35 mm PBGA at location 1 using oprojection moiré system.

All of the applied PBGA packages have daisy chain connections. The initial resistance for each PBGA package is shown in Table II.

The above PWBA samples were placed in the ESPEC test chamber to perform the standard thermal cycling test [JESD22-A104-A] with the temperature range from −40 °C to 125 °C. Total of 3000 thermal cycles were conducted. Each cycle has the time period of 60 min. Failure of the solder bumps is defined when the resistance value is larger than 300 Ω in this study. 300 Ω is used as a critical value to determine failure of solder bumps in a package because that is the value that was used by most researchers to determine the onset of solder bump failure.

The results of fatigue failures obtained from experiment are shown in Fig. 5. These experimental results show that the initial PWB warpage has significant effects on the low cycle fatigue life of solder bumps on PBGA packages. The higher the initial warpage, the lower the fatigue life of solder bumps on board assemblies. If the maximum initial warpage of the PWB sample changes from 1.706 mm to 3.176 mm, the Sn–Pb solder bump fatigue life of the 27 × 27 mm PBGA package assembled at location 1 is decreased by 26.7%, from 2625 cycles to 1925 cycles. The dimensions and locations of packages also affect the bump fatigue. The solder bumps have longer fatigue life if the package is small in size and is located far from the PWB center. The PWB samples have large deformations near the board center. Therefore, if the electronic package is assembled far from the PWB center, the initial board warpage has less effect on the fatigue life of solder bumps on board assemblies.

During the thermal cycling test, the warpage of the 20 PWBA samples was measured using the projection moiré system after each set number of thermal cycles. Fig. 6 shows the relative warpage change across the PWBs during the thermal cycling test. Each curve represents the average values of the warpage change across five PWB samples populated with 27 × 27 mm PBGA or 35 × 35 mm PBGA, at locations 1 or locations 2. A comparison of the relative warpage changes is provided in the figure for four sets of samples. The experimental results show that as the number of thermal cycles increases, the PWB warpage increases. The assembled PBGA packages constrain the deformation of the PWB samples during thermal cycling process. For the 35 × 35 mm PBGA assembled at location 2, the maximum warpage across the PWB changed 25.7% after 3000 thermal cycles; while for the 35 × 35 mm PBGA assembled at location 1, the maximum warpage across the PWB changed 29.8% after 3000 thermal cycles; for the 27 × 27 mm PBGA assembled at location 2, the maximum warpage across the PWB changed 28.5% after 3000 thermal cycles; while for the 27 × 27 mm PBGA assembled at location 1, the maximum warpage across the PWB changed 33.4% after 3000 thermal cycles. A large size electronic package located near the board center will cause the PWB to warp less than if it were located further away from the center.

V. FINITE ELEMENT MODELING TO ESTIMATE FATIGUE LIFE OF SOLDER BUMPS AFFECTED BY INITIAL PWB WARPAGE

A. Geometric Modeling and Meshing

In order to study the effect of initial PWB warpage on the low cycle fatigue of solder bumps in PBGA packages, 3-D finite element models of PWBA with different degrees of board warpage were used to estimate the solder bump fatigue life for different types of assembled PBGAs using ANSYS 7.1. To improve the
TABLE II
INITIAL RESISTANCE OF DAISY CHAIN PBGA PACKAGES

<table>
<thead>
<tr>
<th>Assembled Package Characteristics</th>
<th>27 × 27 mm PBGA, 63Sn-37Pb, 256 solder bumps</th>
<th>35 × 35 mm PBGA, 63Sn-37Pb, 352 solder bumps</th>
</tr>
</thead>
<tbody>
<tr>
<td>Package Location on PWB Sample</td>
<td>Location 1</td>
<td>Location 1</td>
</tr>
<tr>
<td>Resistance (Ω) of Package on PWB 1</td>
<td>0.3</td>
<td>0.2</td>
</tr>
<tr>
<td>Resistance (Ω) of Package on PWB 2</td>
<td>0.4</td>
<td>0.3</td>
</tr>
<tr>
<td>Resistance (Ω) of Package on PWB 3</td>
<td>0.3</td>
<td>0.2</td>
</tr>
<tr>
<td>Resistance (Ω) of Package on PWB 4</td>
<td>0.3</td>
<td>0.3</td>
</tr>
<tr>
<td>Resistance (Ω) of Package on PWB 5</td>
<td>0.2</td>
<td>0.2</td>
</tr>
</tbody>
</table>

Fig. 5. Solder bump fatigue failure affected by initial PWB warpage.

Fig. 6. Relative warpage change across PWBs during thermal cycling test.

Based on the high resolution of the real-time projection moiré warpage measurement system, the warpage value at any location on the sample surface can be collected at each temperature level. In order to create a 3-D surface map of the PWB warpage, warpage was measured at 45 different locations on the PWB at the eutectic temperatures of 183 °C (Sn–Pb) and 217 °C (lead-free), respectively. Those measured points are distributed over the PWB surface uniformly. The warpage values for the 45 specified points were inputted into finite element method (FEM) to generate the accurate geometry of the PWB surface with warpage for each sample at each temperature level. SHELL91 elements were used to mesh the PWB surface for their advantages in modeling layered shell structures. Two types of PBGA packages, including package molding, BT substrate, and solder bumps, were modeled and meshed using SOLID95 elements, which are 3-D 20-node solid elements. Fig. 7 shows the meshed PWB with a 35 × 35 mm PBGA package.

B. Material Properties for Modeling

The material properties used in the PWBA models are shown in Tables IV and V [4]–[6]. Two types of solder bumps were simulated in the models: one is eutectic 63Sn-37Pb alloy, and the other is 95.5Sn-3.8Ag-0.7Cu alloy. They were both modeled as nonlinear, elastic-plastic-creep, isotropic, and temperature dependent. In order to obtain accurate simulation, temperature dependent material properties for Sn–Pb and lead-free solder obtained from the stress-strain curves in [5], [6] were used in the FE models.

C. Boundary Constraints and Thermal Cycling Tests

In the FEA model, the PWB length is in the X-direction, the PWB width is in the Y-direction, and the PWB thickness is in the Z-direction. Along the width directions (Y-directions) of the PWB, the Z-displacement in each node is zero, because during the testing, the PWB is supported on the width directions with two invar rods. Therefore, to prevent any motion of the PWBA, there is a constraint in the Z-direction along the width edges of the PWB. The FE simulation was utilized to investigate the solder bump fatigue reliability under the standard thermal cycle conditions [JESD22-A104-A], which is also used in the experimental study. The structure was assumed to be stress free at 183 °C for 63Sn-37Pb solder bumps, and at 217 °C...
can be calculated according to (2) [7]

\[ \varepsilon_{eff} = \frac{\sqrt{3}}{3} (\varepsilon_1 - \varepsilon_2)^2 + (\varepsilon_2 - \varepsilon_3)^2 + (\varepsilon_3 - \varepsilon_1)^2 \]

where \( \varepsilon_i \) (\( i = 1, 2, 3 \)) are the principal strains.

The effective strain range is the difference in the two strains resulting from two extreme temperatures in each thermal cycle. The maximum effective strain value of the solder bumps was used for fatigue estimation. An empirical relationship between plastic strain and bump life, proposed by Coffin–Manson [8], has been widely used to predict solder bump fatigue life

\[ \Delta \varepsilon_p = \varepsilon_f (2N_f)^c \]

where \( \Delta \varepsilon_p \) represents the plastic strain range, \( N_f \) is the total number of cycles to failure, \( \varepsilon_f (\approx 0.325) \) is the fatigue ductility coefficient, and \( c (-0.7 \sim -0.5) \) is the fatigue ductility exponent.

### E. Model Validation

To validate the FE model, the PWB test sample with one 35 x 35 mm 63Sn-37Pb PBGA package shown in Fig. 2 was used. The FE model went through the first four temperature loading steps in the thermal cycling simulation: from 183 °C to room temperature in 150 s, then heated up to 125 °C, while keeping the dwell time as 10 min at 25 °C and 125 °C. The PWB warpage entered into the FE model had a maximum value of 995.1 µm at 183 °C, which was the measurement result from the projection moiré experimental system using the corresponding PWB sample. At each end of the temperature load steps, the maximum PWB out-of-plane displacements from projection moiré experiments and FE model were compared and are shown in Table VI. This data shows that the maximum difference between experimental and modeling results is 7.8%. The FE model results agreed well with the corresponding experimental results, and can be used to study the correlation between PWB warpage and low cycle fatigue life of solder bumps on board assemblies.

### F. Effect of PWB Warpage on Solder Bump Fatigue

In this section, the FE models developed in previous sections were used to study the fatigue life of solder bumps on board assemblies affected by initial PWB warpage. Two types of PBGA packages, 27 x 27 mm and 35 x 35 mm packages, were used. Each PBGA package has Sn–Pb and Sn-Ag-Cu solder bumps, and can be placed at three different locations: location 1 is at the PWB center, location 2 is at a horizontal distance of 60 mm and a vertical distance of 30 mm from the PWB center, and location 3 is at a horizontal distance of 78 mm and a vertical distance of 46 mm from the PWB center. The third location has the longest distance of 91 mm between the PWB center and package center. Based on the projection moiré measurement results, three different PBGA samples as shown in Table III were modeled. The measured warpage values were entered into the FEM to improve the accuracy of the PWB surface geometry at the starting temperature. The shape of the PWB warpage, convex up (+) or concave up (−), was also considered as shown in Fig. 8, i.e., the PBGA package can be assembled on both sides of each PWB.

The fatigue assessment of the solder bumps with maximum strain on board assemblies for 27 x 27 mm and 35 x 35 mm PBGA packages with Sn–Pb and Sn-Ag-Cu solder are shown in Figs. 9–12. The FE simulation results are consistent with the above thermal cycling experimental results. For a PWB with higher initial warpage, the fatigue life of solder bumps on board assemblies is reduced. A PWB with concave shape will decrease the reliability of solder bumps more than a PWB with convex shape. In addition, lead-free solder bumps are much more reliable than Sn–Pb solder bumps [9].
VI. DEVELOPMENT OF STATISTICAL ANALYSIS AND PREDICTION MODEL

A. Design of Simulations and Regression Model

In order to study the correlation between initial PWB warpage and solder bump fatigue reliability, a five-factor design of simulations and a regression model were developed for Sn–Pb and lead-free solder bumps based on the FEA solutions. In the regression model, the response is the fatigue life time of solder bumps; the factors of interest are the maximum initial PWB warpage at room temperature, warpage shape, PBGA package dimension, location of package, and solder bump material. The PWB warpage shape can be convex up (+1) or concave up (−1).
where \( N_r \) is fatigue life estimation of solder bumps (cycles); \( w_{\text{max}} \) is maximum initial PWB warpage at 25 \( ^\circ \text{C} \) (mm); \( w_{\text{shape}} \) is warpage shape; \( d_p \) is package dimension (mm); \( l_p \) is location of package (mm); \( m \) is solder bump material.

### B. Advanced Model Fitting Based on FE Simulation Results

Regression analysis in Section IV-A is intuitive and simple. However, there are some drawbacks. First, linear regression models usually assume random errors in the output. This is not true for most of the FE simulations because they usually yield a deterministic answer for a given set of inputs. Second, the underlying system may be very complicated which can not be well approximated by the simple linear model. Therefore, more flexible nonlinear metamodels (prediction models) are called for.

The kriging model is the most popular method used for obtaining metamodels based on computer simulations [11]. It is popular because it obtains interpolating metamodels which are suitable for conducting deterministic simulations. Assume the true function \( N_k(x) \), is a realization from a stochastic process. A commonly used kriging model, known as ordinary kriging, can be stated as follows:

\[
N_k(x) = \mu_0 + Z(x)
\]

where \( \mu_0 \) is an unknown parameter, and \( Z(x) \) is a weak stationary stochastic process with a mean of 0 and a covariance function of \( \sigma^2 \).

To calculate the metamodel (or predictor), assume that the true function is evaluated at \( m \) inputs \( \{x_1, \ldots, x_m\} \), and \( N_{\text{out}} = (N_1, \ldots, N_m)^T \) is the corresponding outputs. For an unobserved input \( x \), the ordinary kriging predictor is shown in

\[
\hat{N}_k(x) = \hat{\mu}_0 + \varphi(x)^T \Psi^{-1}(N_{\text{out}} - \hat{\mu}_0 I)
\]

where \( I \) is a column of 1’s with a length of \( m \), \( \varphi(x) = (\varphi(x - x_1), \ldots, \varphi(x - x_m))^T \), \( \Psi \) is an \( m \times m \) matrix with the \( j \)th element of \( \varphi(x - x_j) \), and \( \hat{\mu}_0 = I^T \Psi^{-1} N_{\text{out}}/I^T \Psi^{-1} I \).

The most popular correlation function, Gaussian product correlation function is used in this research to solve for \( \varphi \) in kriging model as shown in

\[
\varphi(h_i) = \exp\left(-\sum_{j=1}^{p} \theta_j h_{ij}^2\right), \quad \Theta = (\theta_1, \ldots, \theta_p)^T.
\]

In order to better understand the effect of the package assembly location in this research, three different locations were investigated with their exact coordinates in horizontal direction and vertical direction (\( H_i \) and \( V_i \)) with respect to the PWB center. In addition, one more factor was added into the prediction model to denote the existence of initial PWB warpage. Therefore, a total of seven factors (\( p = 7 \)) were considered in the kriging model. These factors are denoted by the vector \( x \). The correlation parameters (coefficients of the factors in \( x \) vector), \( \Theta \), were estimated by maximizing the likelihood as shown in (8) [11]. These correlation parameters (coefficients) used in the

The length of the package, which represents its longest side, is always referred to as the dimension of the package, \( d_p \). The location of the package is determined from the PWB center to the package center. In (4), whenever the solder bump material is Sn–Pb, “−1” is substituted for \( m_s \), and whenever the bump material is Sn–Ag–Cu, “+1” is substituted for \( m_s \). Equation (4) shows a reduced bilinear model expressing the response as a function of the five predictor variables and their two-factorial interactions [10]

\[
N_r = 1855 - 165w_{\text{max}} + 12d_p + 148m_s + 5.94w_{\text{max}}w_{\text{shape}} + 0.219w_{\text{max}}d_p + 0.0263d_p^2 \tag{4}
\]
The reliability of solder bumps is more reliable than the Sn–Pb solder with the value of solder bump fatigue life. The package location materials ability. Moreover, there are two significant interactions between PWB warpage shape have small effects on the solder bump reliability. The recurrence of initial board warpage, the solder bump fatigue reliability of solder bumps, the sensitivity analysis technique was applied on the ordinary kriging predictor as shown in (11), more experimental fatigue reliability data was predicted and compared with the experimental results from the accelerated thermal cycling tests. Table VII shows four different comparisons. The maximum difference in solder bump fatigue life is higher with Sn–Pb solder than with lead-free material. Compared with the lead-free solder, Sn–Pb solder can produce more effects on the fatigue life when the warpage shape is changed.

C. Analysis of Advanced Integrating Model

In the previous sections, the effect of initial PWB warpage on solder bump fatigue reliability was studied based on FE simulation results. However, the simulation results are not as accurate as the experimental results. In order to effectively integrate the simulation and experimental data, a location adjustment model was developed as shown in (10) [12]. The idea is to first fit a Gaussian process model using only the simulation results, and then adjust this model by limited physical experimental results.

\[ N_f(\tilde{\varphi}) = N_k(\tilde{\varphi}) + \delta(\tilde{\varphi}) + \varepsilon \tag{10} \]

where \( N_f(\tilde{\varphi}) \) represents the experimental results, \( N_k(\tilde{\varphi}) \) represents the FE simulation results, \( \varepsilon \) is an additive error which follows a normal distribution with a mean of 0 and a variance of \( \sigma^2 \), and \( \delta(\tilde{\varphi}) \) is the location adjustment term.

In practice, the \( N_f(\tilde{\varphi}) \) and \( N_k(\tilde{\varphi}) \) may not share the same input values. This problem can be easily overcome by replacing \( N_k(\tilde{\varphi}) \) with \( \tilde{N}_k(\tilde{\varphi}) \) estimated by (9). The location adjustment term is estimated by \( \delta(\tilde{\varphi}) = 1830.3 - 540u_{\text{max}} \) using the forward selection technique with \( R^2 \) at 0.7. Therefore, the final integrated model to predict solder bump fatigue life was obtained as shown in

\[ \tilde{N}_f(\tilde{\varphi}) = 2031.9 - 540u_{\text{max}} + \varphi(\tilde{\varphi})^T \Psi^{-1}(N_{\text{out}} - 1101.6I) \tag{11} \]

This advanced integrating model is effectively optimized with the experimental data of Sn–Pb solder bump fatigue life discussed in Section IV. It has higher accuracy to evaluate the bump reliability than the prediction model in (4) and (9). Based on the kriging model shown in (11), solder bump fatigue reliability was predicted and compared with the experimental results from the accelerated thermal cycling tests. Table VII shows four different comparisons. The maximum difference in solder bump fatigue life between the kriging prediction model and the experimental results is 6.1%. Therefore, the kriging prediction model as shown in (11) is able to successfully estimate the solder bump fatigue reliability affected by PWB warpage with very high accuracy. To further increasing the accuracy of the advanced prediction model in (11), more experimental fatigue reliability data of lead-free solder bumps and data comparisons are needed.
TABLE VII
SOLDER BUMP FATIGUE RELIABILITY COMPARISON BETWEEN PREDICTION MODEL AND EXPERIMENTS FOR DIFFERENT CASES

<table>
<thead>
<tr>
<th>Case</th>
<th>Max. Initial Warpage across PWB at Room Temperature (mm)</th>
<th>PBGA Dimension (mm)</th>
<th>Distance from PBGA Center to Board Center (mm)</th>
<th>Fatigue Life from Prediction Model (Cycles)</th>
<th>Experimental Fatigue Life (Cycles)</th>
<th>Difference</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1.833</td>
<td>27 × 27</td>
<td>67</td>
<td>2633</td>
<td>2750</td>
<td>-4.3 %</td>
</tr>
<tr>
<td>2</td>
<td>2.013</td>
<td>27 × 27</td>
<td>30</td>
<td>2465</td>
<td>2625</td>
<td>-6.1 %</td>
</tr>
<tr>
<td>3</td>
<td>2.171</td>
<td>35 × 35</td>
<td>67</td>
<td>2507</td>
<td>2400</td>
<td>4.4 %</td>
</tr>
<tr>
<td>4</td>
<td>2.425</td>
<td>35 × 35</td>
<td>30</td>
<td>2277</td>
<td>2250</td>
<td>1.2 %</td>
</tr>
</tbody>
</table>

VII. CONCLUSION
In this research, the effect of initial PWB warpage on the low cycle fatigue of the solder bumps on PBGA packages was studied based on the projection moiré measurement system and finite element modeling. This study successfully documents the correlation between initial PWB warpage and fatigue reliability of solder bumps on board assemblies. The projection moiré system and accelerated thermal cycling tests were used to investigate how initial PWB warpage affects the solder bump fatigue on board assemblies. The experimental results show that for a PWB with higher initial warpage, the fatigue life of solder bumps on board assemblies is reduced. The solder bumps have longer fatigue life if the package is small in size and is located far from the PWB center. In addition, with increasing the number of thermal cycles, the PWB warpage increases. The assembled PBGA packages constrain the deformation of the PWB samples during thermal cyclic conditions. Meanwhile, 3-D FE models were developed to predict the solder bump fatigue reliability affected by initial PWB warpage. The simulation results are consistent with the above experimental results. They also show that a PWB with concave shape will decrease the reliability of solder bumps more than a PWB with convex shape. The dimensions and locations of packages, and solder bump materials also affect the bump fatigue life. The above guidelines can be used to estimate the thermomechanical reliability of solder bumps on board assemblies with initial PWB warpage.

Design of experiments and an advanced prediction model were developed to study the correlation between initial PWB warpage and solder bump fatigue reliability based on experimental results and the FEA solutions. An advanced kriging model was developed and used to investigate the effects of initial PWB warpage on solder bump thermal fatigue reliability with very high accuracy.

REFERENCES

Authors’ photographs and biographies not available at the time of publication.